Transient Voltage Suppression Diodes





Description

Transient Voltage Suppressor (TVS) is a circuit protection component that either attenuates (reduces) or filters a transient voltage spike (overvoltage), TVS diodes provide critical protection by going into avalanche breakdown within no more than a few nanoseconds after a strike, clamping the transient voltage, and routing its current to the ground.

Applications

- Communication Equipment
- Security & Protection
- Industrial Control Equipment
- Power Supply
- Automotive Electronics
- New Energy
- Lightning Protection

Functional Diagram



Features

- Junction passivation optimized design passivated anisotropic rectifier technology
- T_J = 175 °C capability suitable for high reliability and automotive requirement
- Available in uni-directional polarity only
- Low leakage current
- Low forward voltage drop
- High surge capability
- Meets ISO16750-2 surge specification(varied by test condition)
- Meets MSL-1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified
- Compliant to RoHS directive 2011/65/EU and in accordance to WEEE 2002/96/EC

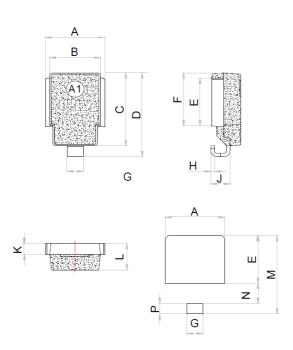
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Package Outline Dimensions (DO-218AB)



	Millime	eters	Inches	
Symbol	Min.	Max.	Min.	Max.
Α	9.5	10.5	0.374	0.413
В	8.3	8.7	0.327	0.342
С	13.3	13.7	0.524	0.539
D	15.0	16.0	0.592	0.628
E	8.5	9.1	0.335	0.358
F	9.5	10.1	0.374	0.398
G	2.4	3.0	0.094	0.118
Н	0.5	0.7	0.020	0.028
J	2.7	3.7	0.106	0.146
K	1.9	2.1	0.075	0.083
L	4.7	5.1	0.185	0.201
М	14.2	14.8	0.559	0.583
N	3.5	4.1	0.138	0.161
Р	1.6	2.2	0.063	0.087



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Maximum Ratings and Characteristics

(Ratings at 25°C ambient temperature unless otherwise specified.)

Parameter	Symbol	Value	Unit
Peak pulse power dissipation on 10/1000 μS waveform	P _{PPM}	6600	W
Peak pulse power dissipation on 10/10000 μS waveform	P _{PPM}	5200	W
Peak Power Dissipation on Infinite Heat Sink at T _C =50 °C	P _D	8.0	W
Peak pulse current with 10/1000 μS waveform	I _{PPM}	See page 6	А
Peak forward surge current,8.3ms single half sine-wave	I _{FSM}	700	Α
Operating junction and storage temperature range	T _J ,T _{STG}	-55 to 175	°C
Typical Thermal Resistance Junction to Lead	$R_{\theta JL}$	0.9	°C/W
Typical Thermal Resistance Junction to Ambient	R _{0JA}	12	°C/W

Note

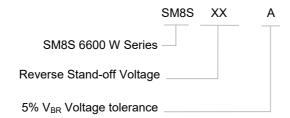
Non-repetitive current pulse derated above TA = 25 °C.

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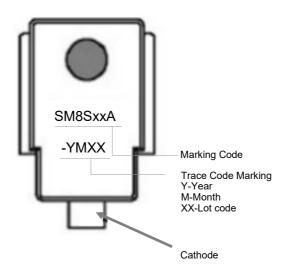


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Part Numbering System



Marking



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Glossary

Item	Description					
	Clamping Voltage					
V _C	Voltage across TVS in a region of low differential resistance that serves to limit the voltage across the device terminals.					
	Reverse Stand-off Voltage					
	Maximum voltage that can be applied to the TVS without operation.					
V _R	NOTE : It is also shown as V_{WM} (maximum working voltage (maximum d.c. voltage)) and known as rated stand-					
	off voltage (V_{so}).					
	Reverse Leakage Current					
I _R	Current measured at V_{R}					
	NOTE : Also shown as $I_{\mathbb{D}}$ for stand-by current.					
V BR	Breakdown Voltage					
VBR	Voltage across TVS at a specified current I_T in the breakdown region.					
I PPM	Rated Random Recurring Peak Impulse Current					
PPM	Maximum-rated value of random recurring peak impulse current that may be applied to a device.					
	Rated Average Power Dissipation					
$P_{M(AV)}$	Maximum-rated value of power dissipation resulting from all sources, including transients and standby current,					
	averaged over a short period of time.					
	Rated Random Recurring Peak Impulse Power Dissipation					
P _{PPM}	Maximum-rated value of the product of rated random recurring peak impulse current (I _{PPM}) multiplies by specified					
	maximum clamping voltage ($V_{\mathbb{C}}$).					
C _J	Capacitance					
	Capacitance across the TVS measured at a specified frequency and voltage.					
	Peak Forward Surge Voltage					
V _{FS}	Peak voltage across an TVS for a specified forward surge current (I_{FS}) and time duration.					
	NOTE : Also shown as V _F .					
,	Forward Surge Current Pulsed current through TVS in the forward conducting region.					
I _{FS}	NOTE : Also shown as $I_{\rm E}$					
	Temperature Coefficient of Breakdown Voltage					
α _{V(BR)}	The change of breakdown voltage divided by the change of temperature.					
	Peak pulse Current					
I _{PP}	Peak pulse current value applied across the TVS to determine the clamping voltage $V_{\mathbb{C}}$ for a specified wave shape.					
	Pulsed D.C. Test Current					
	Test current for measurement of the breakdown voltage V_{BR} . This is defined by the manufacturer and usually					
<i>I</i> _T	given in milliamperes with a pulse duration of less than 40 ms.					
	NOTE : Also shown as I_{BR}					
	—(GB-T 18802.321 / IEC 61643-321 / JESD210A)					

—(GB-T 18802.321 / IEC 61643-321 / JESD210A)

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Electrical Characteristics (T_A=25 °C unless otherwise noted)Table 1

Part Number	Break Volta V _{BR} (age	Test Current I _T	Reverse Stand-off Voltage V _R	Max. Reverse Leakage I _ℝ @V _ℝ		Max. Peak Pulse Current	Max. Clamping Voltage V _c @l _{PPM}
	Min	Max					I _{PPM}	
Uni	(V	')	(mA)	(V)	(μ A @ 25 °C)	(μ A @ 175 °C)	(A)	(V)
SM8S10A	11.1	12.3	5	10	5	250	388	17.0
SM8S11A	12.2	13.5	5	11	5	150	363	18.2
SM8S12A	13.3	14.7	5	12	5	150	332	19.9
SM8S13A	14.4	15.9	5	13	5	150	307	21.5
SM8S14A	15.6	17.2	5	14	5	150	284	23.2
SM8S15A	16.7	18.5	5	15	5	150	270	24.4
SM8S16A	17.8	19.7	5	16	5	150	253	26.0
SM8S17A	18.9	20.9	5	17	5	150	239	27.6
SM8S18A	20.0	22.1	5	18	5	150	226	29.2
SM8S20A	22.2	24.5	5	20	5	150	204	32.4
SM8S22A	24.4	26.9	5	22	5	150	186	35.5
SM8S24A	26.7	29.5	5	24	5	150	170	38.9
SM8S26A	28.9	31.9	5	26	5	150	157	42.1
SM8S28A	31.1	34.4	5	28	5	150	145	45.4
SM8S30A	33.3	36.8	5	30	5	150	136	48.4
SM8S32A	35.5	39.4	5	32	5	150	128.5	51.4
SM8S33A	36.7	40.6	5	33	5	150	124	53.3
SM8S36A	40.0	44.2	5	36	5	150	114	58.1
SM8S40A	44.4	49.1	5	40	5	150	102	64.5
SM8S43A	47.8	52.8	5	43	5	150	95.1	69.4

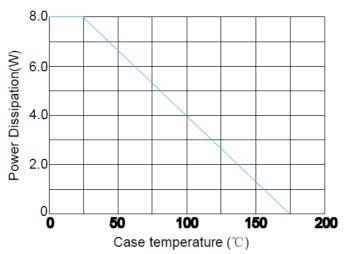
Note

For all types maximum V_F=1.8 V at I=100 A measured on 8.3 ms single half sine-wave or equivalent square wave, duty cycle=4 pulses per minute maximum.

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Performance Curve for Reference(T_A=25 °C unless otherwise noted)



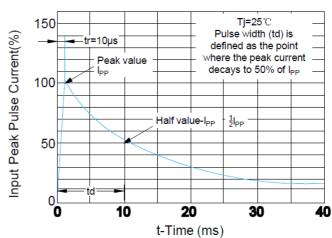
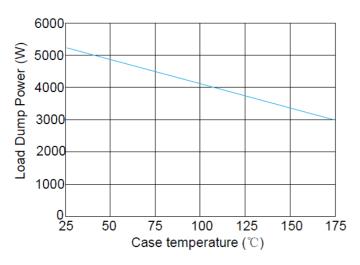


FIGURE 1 Power Derating Curve

FIGURE 2 Pulse Waveform



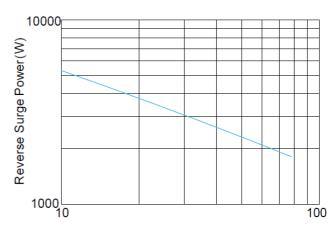
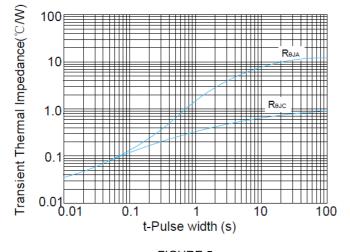


FIGURE 3 Load Dump Power Characteristics (10 mS Exponential Waveform)

FIGURE 4
Reverse Power Capability



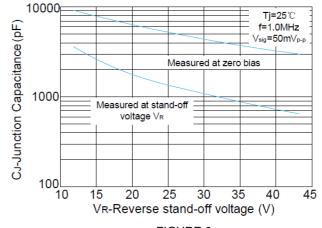
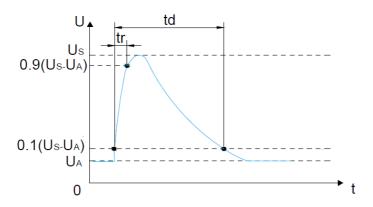


FIGURE 5
Typical Transient Thermal Impedance

FIGURE 6
Typical Junction Capacitance



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Parameter	12V system	24V system
Us	79~101V	151~202V
Ri	0.5~4Ω	1~8Ω
td	40~400ms	100~350ms
tr	5~10ms	5~10ms

FIGURE 7 ISO16750-2 Test Pulse 5 A

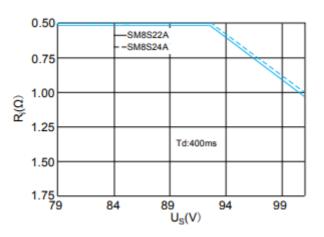


FIGURE 8 Parameters For Test Pulse 5 A

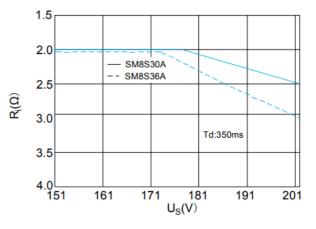


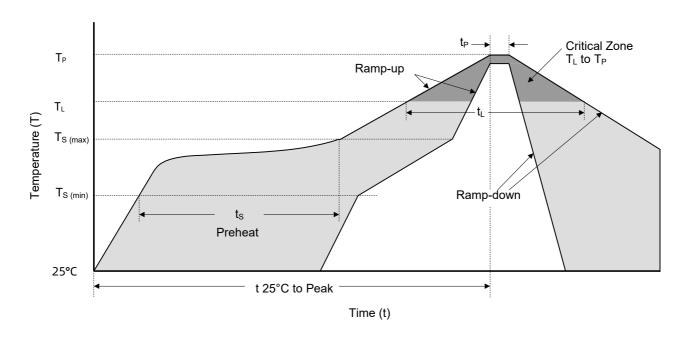
FIGURE 9 Typical SOA Chart ISO16750-25 A 12 V Test System

FIGURE 10 Typical SOA Chart ISO16750-25 A 24 V Test System

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Soldering Parameters



Reflowing Condition

Reflow Solderin	Lead-Free Assembly		
	Temperature Min (T _{S (min)})	150 °C	
Pre-heat	Temperature Max (T _{S (max)})	200 °C	
	Time (min to max) (t _s)	60 ~ 180 seconds	
Average Ramp Up Rate (Li	Average Ramp Up Rate (Liquidus Temp (TL) to Peak		
T _s (max) to T _L	Ramp-up Rate	3 °C / second max.	
5.4	Temperature (T _L) (Liquidus)	217 °C	
Reflow	Time (min to max) (t _∟)	60 ~ 150 seconds	
Peak Tempe	260 ^{+0/-5} °C		
Time of within 5 °C of Actu	Time of within 5 °C of Actual Peak Temperature (t _P)		
Ramp-do	6 °C / second max.		
Time from 25 °C to	8 Minutes max.		
Do Not	260 °C		

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Packaging Information

Tape	Symbol	Dimension		
Tape		Millimeters	Inches	
	A ₀	10.8 ± 0.3	0.425 ± 0.012	
O ₀ P ₀ P ₂	B ₀	16.13±0.3	0.635 ± 0.012	
ш О О О О О О О О О О О О О О О О О О О	С	330.0 ± 0.3	13.0 ± 0.012	
	D ₀	1.55 ± 0.2	0.061 ± 0.008	
A ₀ P ₁	D ₁	1.55 ± 0.2	0.061 ± 0.008	
	E	1.75 ± 0.2	0.069 ± 0.008	
	E ₁	13.30 ± 0.2	0.524 ± 0.008	
	F	11.50 ± 0.2	0.453 ± 0.008	
C	P ₀	4.00 ± 0.2	0.157 ± 0.008	
E ₁	P ₁	16.00 ± 0.2	0.630 ± 0.008	
W ₁ Direction of Feed	P ₂	2.00 ± 0.2	0.635 ± 0.012 13.0 ± 0.012 0.061 ± 0.008 0.061 ± 0.008 0.069 ± 0.008 0.524 ± 0.008 0.453 ± 0.008 0.157 ± 0.008	
1	W	24.00 ± 0.2	0.945 ± 0.008	
	W_1	25.85 ± 0.2	1.018 ± 0.008	

Part Number	Package	QTY (Reel)	Packaging Option	Packaging Specification
SM8SxxA	DO-218AB	750 PCS	Tape & Reel 13" reel	EIA STD RS-481

Transient Voltage Suppression Diodes





Usage

- 1.TVS must be operated in the specified ambient temp.
- 2.Do not clean the TVS with strong polar solvent such as ketone, esters, benzene and halogenated hydrocarbon, to avoid damaging the encapsulating layer.
- 3.Please do not apply severe vibration, shock or pressure to TVS, to avoid element cracking.

Replacement

- 1.If TVS is visually damaged, please replace it.
- 2.TVS is a non-repairable product. For safety sake, please use equivalent TVS for replacement.

Storage

- 1.Storage Temp. Range: (-55 to 150) °C.
- 2.Do not store the TVS at the high temp., high humidity or corrosive gas environment, to avoid influencing the solder- ability of the lead wires. The product shall be used up within 1 year after receiving the goods.

Environmental Conditions

- 1.TVS should not be exposed to the open air, nor direct sunshine.
- 2.TVS should avoid rain, water vapor or other condition of high temp. and high humidity.
- 3.TVS should avoid sand dust, salt mist, or other harmful gases.

Max. Typical Capacitance of TVS

The typical capacitance of TVS is listed in the specifications. Designers may refer to it when designing TVS in High frequency circuit.

Installation Mechanical Stress

- 1.Do not knock TVS when installing, to avoid mechanical damage.
- 2. Please do not apply severe vibration, shock or pressure to TVS, to avoid surface resin or element cracking.